

RESEARCH PROFILE

Name Dr. E. SAKTHIVEL
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EDUCATIONAL PROFILE

| Category | Name of the Degree | Specialization | Year of Passing | Name of the College | Name of the University | % of Marks obtained | Class obtained |
|----------|--------------------|--|-----------------|--|----------------------------|---------------------|----------------|
| UG | BE | Electrical and Electronics Engineering | 2001 | Madurai Kamaraj University | Madurai Kamaraj University | 75% | I |
| PG | ME | High Voltage Engineering | 2005 | Anna University Regional Campous Tirunelveli | Anna University | 82% | I |
| PH.D | PH.D | Power Systems Engineeirng | 2012 | Anna university Chennai | Anna University | Y | - |

Ph. D. Thesis : Performance Analysis of power Reduction in NOC Architecture

RESEARCH PROFILE

✚ No. of publications (As on November 2019)

- Journal:12
- International Conference:
- National Conference:
- Editorial Board:
- Book: Citation

✚ Details:

| ✚ Scopus: | ✚ Web of Science | ✚ Google Scholar: |
|-------------|------------------|-------------------|
| ○ Article: | ○ Article: | ○ Article: 2 |
| ○ Citation: | ○ Citation: | ○ Citation: 48 |
| ○ h index: | ○ h index: | ○ h index: 04 |

LIST OF PUBLICATIONS

JOURNALS:

1. K.Punitha, DDevaraj, **S.Sakthivel**, “Artificial neural network based modified incremental conductance algorithm for maximum power point tracking in photovoltaic system under partial shading conditions”, Energy, Vol. 62, No.330-340, 2013.
2. K.Punitha,Devaraj,**SSakthivel**, “Development and analysis of adaptive fuzzy controllers for photovoltaic system under varying atmospheric and partial shading condition”, Applied Soft Computing, Vol.13, No. 11, 2013.
3. **E.Sakthivel Velsamy**, Malathi, Muruganathanm, Arunraja “Butha: Boost Up Clock Terminal with Heuristic Approach for NoC” Journal of Circuits, Systems, and computers, vol27, No 6(2018).
4. M.Isaivani, V.Malathi, **E.Sakthivel** “An effective interlaced separation vedic multiplier in FPGA platform” International Journal of Computer Sciences and Engineering vol-6,Issue 11 2018.
5. **E.Sakthivel**, M.Arunraja,K.D.Uma,T.Shanthi and A.Muthukrishnan “ Design of optimized logic interface for network-on-chip Architectures” Circuits and systems 2018.
6. R.Sathishkumar,V.Malathi,**E.Sakthivel** “Real time implementation of quazi Z-source inverter incorporated with renewable energy source” ScienceDirect Energy procedia 117(2017).
7. **Erulappan Sakthivel**, Veluchamy Malathi, **Muruganantham Arunraja** and Govindaraj Perumalvignesh “NABI: Low power, high speed FPGA based Novel approach for bilateral filter” International journal of Innovation and Scientific Research 2016.
8. **Erulappan Sakthivel**, Veluchamy Malathi, **Muruganantham Arunraja** and Govindaraj Perumalvignesh “FSM Based DFS link for Network on chip” Circuits and systems, 2016, 1734-1750.
9. Raveendran Arun Prasath, Parasuraman Ganesh kumar, **Erulappan Sakthivel** “Performance Improvement of hardware/ software Architecture for real- time Bio application using Mpsoc” Intelligent Automation and Soft computing ISSN:1079-8587,2016.
10. **Erulappan Sakthivel**, Kasinathan Pounraj, Veluchamy Malathi, Muruganantham Arunraja and Govindaraj Perumalvignesh “CELLA: FPGA based candidate execution with low latency approach for soft MIMO detector” Circuits and Systems,2016,7,1760-1768.
11. ArunRaja, Muruganantham,Malathi,Velchamy,**Sakthivel Erullappan**, “An Adaptive Prediction Framework for energy efficient wireless sensor Networks”Vol.33 Issu1-4,p199-221,2016.
12. R.Sathishkumar, V.Malathi, **E.Sakthivel** “Real time Implementation of Quasi Z-Source Inverter Incorporated with Renewable Energy source”vol.117, 2017.

RESEARCH SCHOLARS

| S.No. | Candidate Name | Title/Area of Research | Remark |
|-------|----------------|--------------------------------|----------------|
| 1. | P.Meenalochini | Double Functioning of Inverter | DC-I Completed |

RECOGNITIONS:

+ Guest Editor:

| | | |
|-----------------------|-------------------------|--------------------------------------|
| Dr.E.Sakthivel | Kalasalingam University | Symposium ARISTA 2012. 12.10.2012 |
|-----------------------|-------------------------|--------------------------------------|

+ Reviewer:

- IEEE Journal of Photovoltaic
- Applied Energy
- Journal Electrical Engineering
- IET Power Electronics